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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/523,556

02/01/2005

Yosuke Mitani

MAT-8650US

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RATNERPRESTIA  
P O BOX 980  
VALLEY FORGE, PA 19482-0980

EXAMINER

KAPLAN, HAL IRA

ART UNIT

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PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	<b>Application No.</b> 10/523,556	<b>Applicant(s)</b> MITANI ET AL.	
	<b>Examiner</b> Hal I. Kaplan	<b>Art Unit</b> 2836	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 27 March 2008.
- 2a) ☒ This action is **FINAL**.                      2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-9 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-9 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 01 February 2005 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All    b) ☐ Some \*    c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |  |   |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)                     | 4) <input type="checkbox"/> Interview Summary (PTO-413)           |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____                                      |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)          | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____  | 6) <input type="checkbox"/> Other: _____                          |

## DETAILED ACTION

### ***Claim Rejections - 35 USC § 103***

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. The factual inquiries set forth in *Graham v. John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

1. Determining the scope and contents of the prior art.
2. Ascertaining the differences between the prior art and the claims at issue.
3. Resolving the level of ordinary skill in the pertinent art.
4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

3. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

4. Claims 1 and 9 are rejected under 35 U.S.C. 103(a) as being unpatentable over the US patent of Furui (6,652,001) in view of the US patent of Kimura (5,373,226).

As to claim 1, Furui discloses a power supply device for a vehicle comprising: an electronic controller (34) for receiving information in response to a driving status of the vehicle (see column 4, lines 32-38 and 50-54), and for outputting information to a load (4,5) about the function of the load (4,5) based on the information received by the electronic controller (34) (see column 4, lines 45-54); a battery (1) for powering the load (4,5) via the electronic controller (34) (see Figure 1); and an auxiliary power supply for powering the load (4,5) when the battery (1) encounters an abnormality, wherein the auxiliary power supply includes a capacitor unit (32), and a power supply section (33) for allowing the capacitor unit (32) to power the electronic controller (34) when the battery (1) operates either abnormally or normally, wherein an operating status ( $V_c$ ) of the power supply section is monitored during a normal operation of the battery (1) for a given time (see column 5, line 20 - column 6, line 14 and Figure 1). Furui does not disclose the claimed compulsory operating section. Kimura discloses a power supply section (2) and a compulsory operating section (1) for operating the power supply section (see column 6, line 16 - column 10, line 28 and Figure 1). It would have been obvious to one of ordinary skill in the art, at the time of the invention, to have modified Furui by using the power supply section and compulsory operating section of Kimura, because the power supply section and compulsory operating section of Kimura can be realized on a single integrated circuit, thus reducing the size of the system.

Furui in view of Kimura do not disclose a plurality of capacitors or a brake, but it would have been obvious to one of ordinary skill in the art, at the time of the invention, to have used a plurality of capacitors, and to have used the system with a brake,

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because it has been held that mere duplication of parts has no patentable significance unless a new and unexpected result is produced, and the recitations of a brake and brake pedal are intended use recitations which do not carry patentable weight. *In re Harza*, 274 F.2d 669, 124 USPQ 378 (CCPA 1960). See MPEP 2144.04(VI)(B) and 2144.07.

As to claim 9, the power supply section of Kimura is formed of MOS transistors (M1-M11) (see Figure 1).

5. Claims 2-7 are rejected under 35 U.S.C. 103(a) as being unpatentable over Furui in view of Kimura as applied to claim 1 above, and further in view of the US patent of Masegi et al. (5,045,835).

As to claim 2, Furui in view of Kimura disclose all of the claimed features, as set forth above, except for the monitoring being halted when the battery or auxiliary power supply operates abnormally. Masegi discloses a similar system in which monitoring the operating status (Vc) of the power supply section is halted when the battery operates abnormally (see column 3, line 64 - column 4, line 7). It would have been obvious to one of ordinary skill in the art, at the time of the invention, to have modified Furui in view of Kimura to halt the monitoring when the battery operates abnormally, in order to conserve power.

As to claim 3, Masegi discloses an output detector (300) for detecting an output voltage (Vc) from the capacitor unit (9), wherein the output detector (300) is used for monitoring the operating status of the power supply section (see column 3, lines 17-18 and 47-52, and Figure 1).

As to claim 4, the output detector (300) of Masegi detects the output voltage (Vc) from the capacitor unit (9) after the power supply section starts operating (see column 3, lines 47-52).

As to claim 5, whether or not the power supply section of Masegi is defective is determined by a comparison of an output voltage (Vc) detected by the output detector (300) after the power supply section operates for a given time with a given reference voltage (V1,V2) (see column 3, lines 47-52).

As to claim 6, the determination about whether or not the power supply system is defective is carried out periodically (whenever ignition switch 2 is opened) (see column 3, lines 40-66).

As to claim 7, when the determination of Furui finds that the power supply section (33) operates abnormally, information (S1,S2) about the abnormality is supplied to the electronic controller (19) (see column 3, lines 47-52 and Figure 1).

***Allowable Subject Matter***

6. Claim 8 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

7. The following is a statement of reasons for the indication of allowable subject matter:

Claim 8 contains allowable subject matter because, as noted in the Office action dated January 14, 2008, none of the prior art of record discloses or suggests the determination about whether or not the power supply section is defective not being

carried out when the battery outputs a normal voltage that is lower than or equal to the capacitor voltage, in combination with the remaining claimed features.

***Response to Arguments***

8. Applicant's arguments, see Remarks, filed March 27, 2008, with respect to the objections and rejections of claims 1-9 under 35 U.S.C. 112, second paragraph, have been fully considered and are persuasive. The objections and rejections of claims 1-9 under 35 U.S.C. 112, second paragraph, have been withdrawn.

9. Applicant's arguments filed March 27, 2008, with respect to the rejections of claims 1-7 and 9 under 35 U.S.C. 103(a) have been fully considered but they are not persuasive.

10. As to claim 1, the Applicants state that the cited combination does not disclose or suggest the claimed compulsory operating section. The Examiner respectfully disagrees. The Applicants state that merely checking whether data is stored in memory is not monitoring the power supply section during normal operation. The Examiner agrees; however, the monitoring of the power supply section of Furui occurs before the decision whether to store data in memory is made. The terminal voltage is monitored, and when it is less than a predetermined voltage, the microcomputer is in an inoperable state. If, on the other hand, the terminal voltage is greater than or equal to the predetermined voltage, the microcomputer runs normally, and data is stored in memory.

Although Kimura does not specifically recite or disclose operating the error amplifier (2) for a given time, it is inherent that the error amplifier (2) is run for a given finite amount of time.

***Conclusion***

11. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hal I. Kaplan whose telephone number is 571-272-8587. The examiner can normally be reached on M-F 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Sherry can be reached on 571-272-2084. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.



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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Michael J Sherry/  
Supervisory Patent Examiner, Art Unit 2836

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